

Electronic Patent Application Fee Transmittal

Application Number:	10774990			
Filing Date:	09-Feb-2004			
Title of Invention:	Static timing model for combinatorial gates having clock signal input			
First Named Inventor/Applicant Name:	Matthew J. Amatangelo			
Filer:	Gregory P. Durbin/Elissa Asaro			
Attorney Docket Number:	188082/US			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Request for continued examination	1801	1	810	810
Total in USD (\$)				810